

ABSTRACT OF THE DISCLOSURE

A mixer, which is a single balanced mixer, includes an LO port for inputting an LO signal, an RF port for inputting an RF signal, an IF port for outputting an IF signal, a balun for converting an unbalanced signal into a balanced signal, and a pair of mixer diodes connected in series. The mixer further includes a high-pass filter for blocking LO and IF signals and passing an RF signal and a low-pass filter for blocking LO and RF signals and passing an IF signal.

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